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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

## Complete if Known

Application Number  
Filing Date 10/24/2003  
First Named Inventor WARREN M. FARNWORTH  
Art Unit  
Examiner Name  
Attorney Docket Number 99-0068.3

## U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
V4	A	US- 5,385,636	01/03/1995	Poingt et al.	
	B	US- 5,674,785	10/07/1997	Akram et al.	
	C	US- 5,677,566	10/14/1997	King	
	D	US- 5,739,585	04/14/1998	Akram et al.	
	E	US- 5,763,939	06/09/1998	Yamashita	
	F	US- 5,783,461	07/21/1998	Hembree	
	G	US- 5,811,879	09/22/1998	Akram	
	H	US- 5,834,945	11/10/1998	Akram et al.	
	I	US- 5,885,849	03/23/1999	DiStefano et al.	
	J	US- 5,925,930	07/20/1999	Farnworth et al.	
	K	US- 5,956,605	09/21/1999	Akram et al.	
	L	US- 6,059,173	05/09/2000	Mays et al.	
	M	US- 6,063,647	05/16/2000	Chen et al.	
	N	US- 6,118,179	09/12/2000	Farnworth et al.	
	O	US- 6,180,504B1	01/30/2001	Farnworth et al.	
	P	US- 6,204,080	03/20/2001	Hwang	
	Q	US- 6,297,560	10/02/2001	Capote et al.	
	R	US- 6,352,881	03/05/2002	Nguyen et al.	
V4	S	US- 6,583,354	06/24/2003	Alcoe	

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
V4	T	WO 99/21226	04/29/1999	Elenius et al.		

Examiner  
Signature

N. Y. Eshkov

Date  
Considered

02/16/05

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file 10/24/03

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		Applicati n Number	
		Filing Date	10/24/2003
		First Named Inventor	WARREN M. FARNWORTH
		Art Unit	
		Examiner Name	
Sheet 2	of 2	Attorney Docket Number	99-0068.2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
V4	T	TUMMALA, RAO R., "Chip-to-Package Interconnections", Microelectronics Packaging Handbook, 1989, pgs 366-368, VanNostrand Reinhold (publisher), New York, NY.	✓
V4	U	LAU, JOHN H., Ball Grid Array Technology, 1995, pgs 33, 47, 56, 133, 180, 247, 279, 475, 476-480, McGraw-Hill, Inc., United States.	✓
V4	V	JOHN BALIGA, "Assembly & Packaging Wafer-Level Packages to Include Solder Ball Support", Nov. 27, 2000, pgs 1-2, Technology News.	✓

Examiner Signature	V. Yeishner	Date Considered	02/16/05
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(Use as many sheets as necessary)*

Application Number	10/693,380
Filing Date	10/24/2003
First Named Inventor	WARREN M. FARNWORTH
Art Unit	2812
Examiner Name	
Attorney Docket Number	99-0068.3

Sheet	1	of	1
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Date Considered	02/26/05
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